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The crosstalk paths between pins will inject noise into the resonator tank and degrade the noise performance of the VCO. Also, BSD 68 CMOS PLLs AND VCOs FOR 4G WIRELESS (electro-static discharge) protection networks in CMOS are probably the major factor preventing the implementation of external resonator tanks.

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it has a great impact on the PLL's overall performance. For CMOS based VCO design in current technology, LC and Ring based VCOs are two typical choices used in PLL design [1]. LC VCOs have a superior phase noise performance compared with Ring VCOs. How-ever, an LC VCO has a small tuning range, large layout area and possibly higher power [2].

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